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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
MEARS ET AL.) Examiner: L. PHAM
)
Serial No. 10/647,069) Art Unit: 2814
)
Filing Date: AUGUST 22, 2003) NOA Date: 10/6/04
)
For: SEMICONDUCTOR DEVICE INCLUDING)
MOSFET HAVING BAND-ENGINEERED)
SUPERLATTICE)
)

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. 1.312

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Without withdrawing the above-identified application
from allowance, please amend the application as follows: